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4. Is the drive not going to sleep after it appears to be maintaining pressure below “Shutoff Frequency” (Note: this could be a systemic issue if the drive cannot maintain pressure below Shutoff Frequency and Shutoff Frequency may need to be increased)

## Troubleshooting Transducer Noise

1. Check routing and Installation guidelines above.
2. Try disconnecting the bare shield wire from the ground terminal if it is connected.
3. Disconnect the shield wire from COM
4. On SD/2XD/DX series verify the jumper from COM to Ground is present. If not add the jumper. (Note: This WILL NOT work for active front end LH series drives)

## Troubleshooting “SENSOR CONNECTION FAIL” Fault

1. This fault occurs when the 4-20 mA circuit is reading 0 mA.
2. Check to ensure the correct transducer terminals are being used.
3. Check the connection between the drive and the transducer
4. Check any possible splices in the transducer wire. (Note: We do not recommend splicing transducer wire. If a wire must be spliced the additional wire should be shielded and the splice should be a soldered connection. DO NOT USE WIRE NUTS)
5. Remove the transducer leads and check for 15 VDC between the I1 terminals. If no voltage is present check I2 and switch to the I2 analog input. “Analog Select” under Interface parameters must be changed to “1 Analog 2 Select”
6. On SD/2XD series drives check to see if common mode noise capacitors are installed from BUS+ and BUS- to ground. If not, create an order for **SVK6000** and use DOC023 for installation. See below.

### SD202-SD205

1. Crimp spade terminals on to stripped wire end of both snubber caps.
2. Route Snubber Cap wires from J2 and J3 under divider and board to ground lugs (see photo for wire routing).
3. Do not impede customer to Ground Lugs.

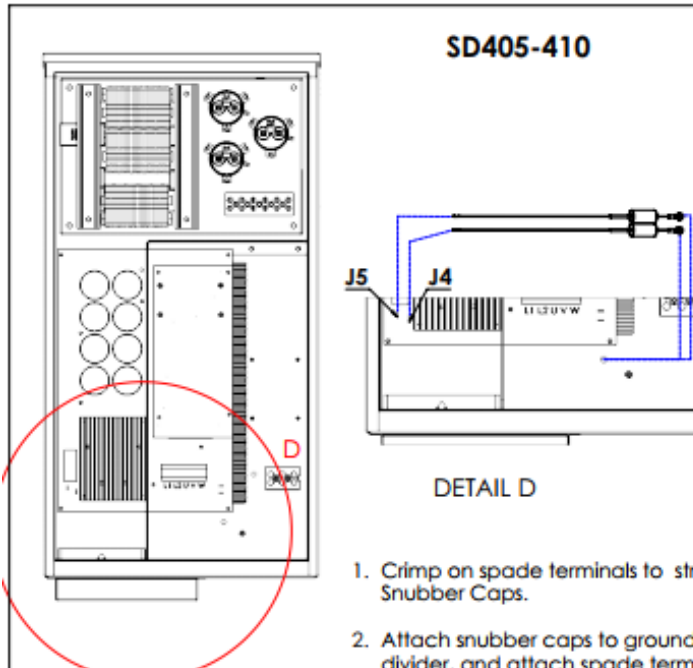
Drawing Title: SD and 2XD Common Mode Caps	Part Number: SDOC023	Revision: 000	Drawn by: NJC	Sheet 1 of 4	Date: 8/13/2020
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### SD207-SD210

1. Cut zip tie holding capacitors together and cut stripped wire down to necessary length. Crimp ring terminals on to stripped wire end of both snubber caps.
2. Route 1st Snubber Cap from JPOS terminal to the mounting hole for the Inverter Board. (See photo 1)
3. Route 2nd Snubber Cap from Input Diode left side to grounding hole in center panel. (See photo 2)

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### SD405-410

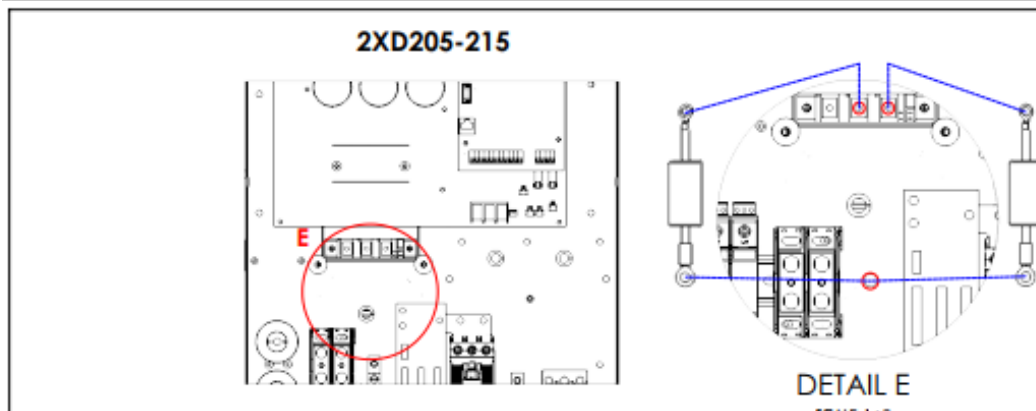


**DETAIL D**

1. Crimp on spade terminals to stripped wire end of both Snubber Caps.
2. Attach snubber caps to ground lug. Route wires under divider, and attach spade terminals to J4 and J5. Do not impede access to Ground Lug.


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### 2XD205-215



**DETAIL E**  
SCALE 1 : 2

1. Cut stripped wire down to length needed and crimp ring terminals on to stripped wire end of both snubber caps.
2. Route Snubber Cap wires from the input diode middle and right terminals to ground lug. (see photo)
3. Do not impede access to Ground Lugs



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7. Add snubber capacitor from I1+ to COM. If this does not work add a second snubber cap between I1+ and I1-.
8. Check to see if an Ashcroft low noise transducer is being used. If not replace with new S00150 low noise transducer.

## Troubleshooting “15V DC OVERLOAD” Fault

1. This fault indicates a short on the transducer circuit
2. Reverse the transducer leads/Verify correct polarity
3. Try switching to the I2 analog input. “Analog Select” under Interface parameters must be changed to “1 Analog 2 Select.”(Note: It is not likely this will help actual transducer noise but if I1 is damaged this may correct)